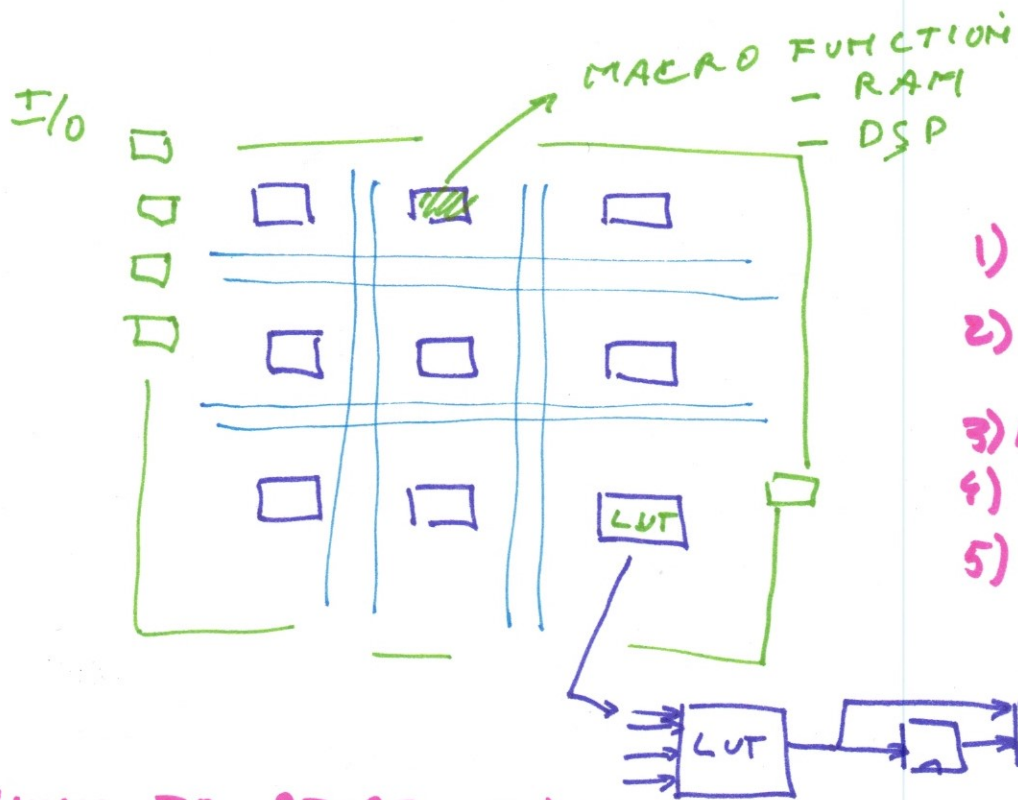


FPGA RECAP

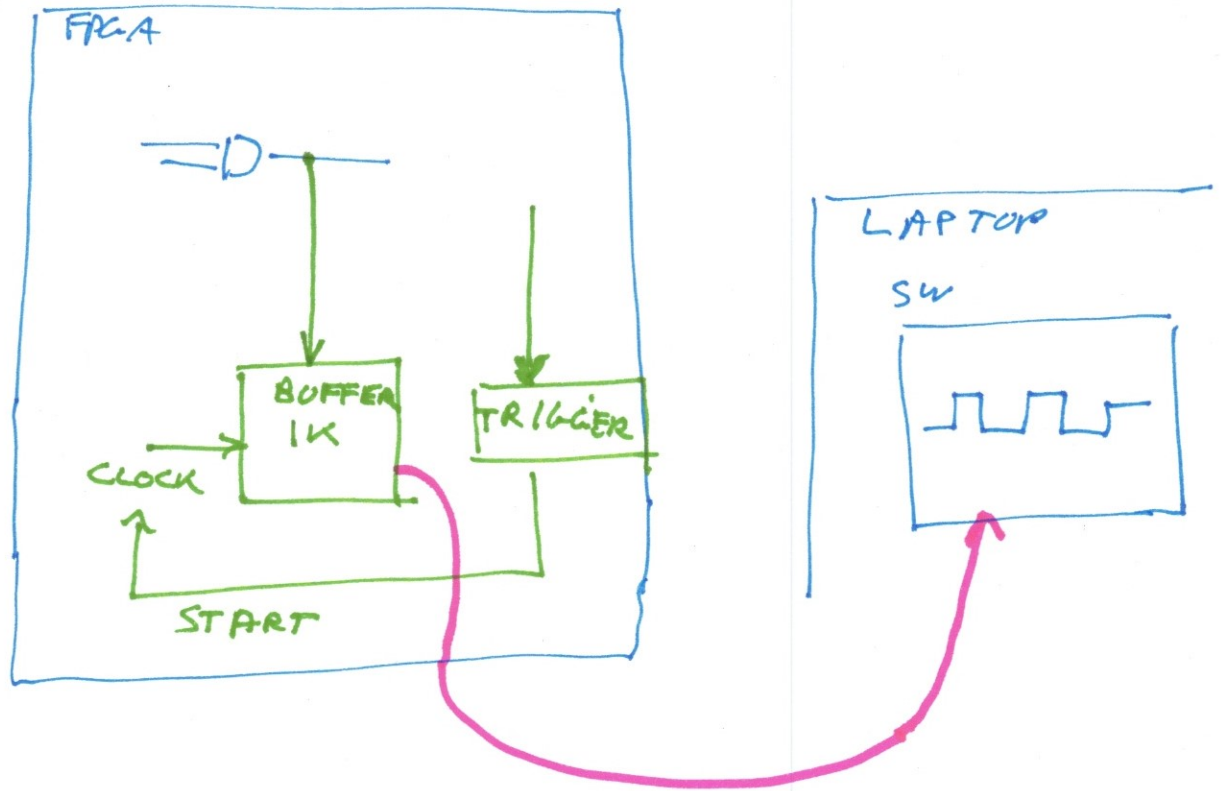


- 1) LUT / FLUP
- 2) NETWORK / INTERCONNECT
- 3) RAM
- 4) DSP
- 5) I/O

HOW TO STORE BITSTREAM ?

- ① SRAM FPGA
- ② NON VOLATILE MEMORY FPGA
- ③ ANTI FUSE FPGA

SIGNAL TAP



HOMEWORK 5

L RCK.

BCLK.

FIR STSAMPLE

64-BIT SR.

START CORDIC.

SAMPLE

64-BIT SR.

