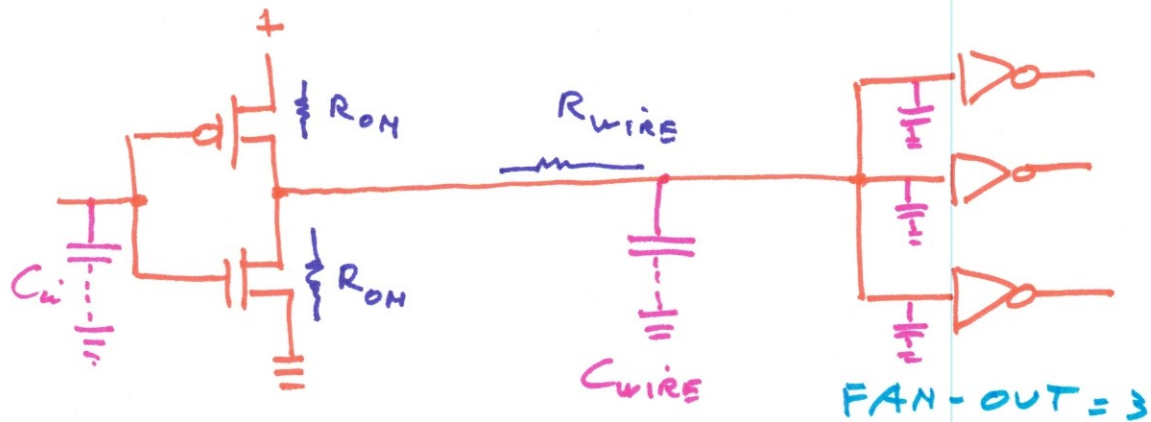


CMOS



FASTER CIRCUITS:

- BIGGER TRANSISTORS
- SHORTER, THICKER WIRES
- LOWER FANOUT

POWER.

$$P = \alpha \cdot C_L \cdot V_{DD}^2 \cdot f + I_{LEAK} \cdot V_{DD}$$

ASIC $\leftrightarrow$ FPGA

