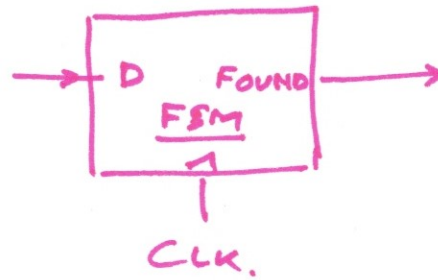


PATTERN RECOGNIZER

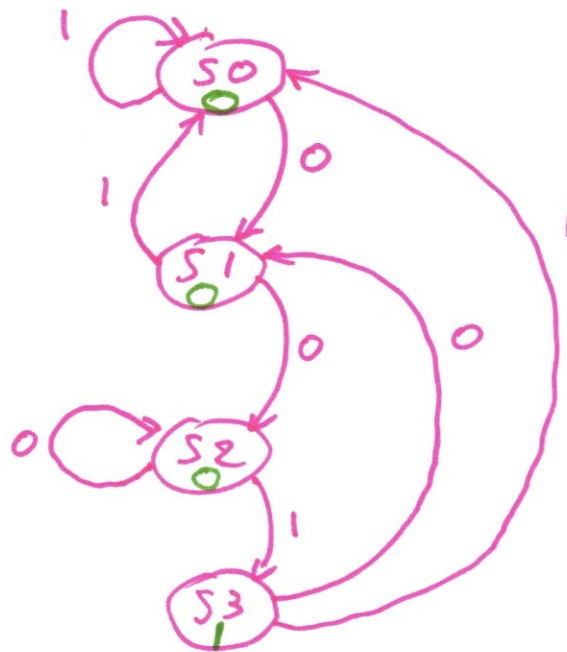
PATTERN: 0 0 1



D	0	1	1	1	0	1	0	0	1	0	0	0
FOUND	0	0	0	0	0	0	0	0	1	0	0	0

STATES:

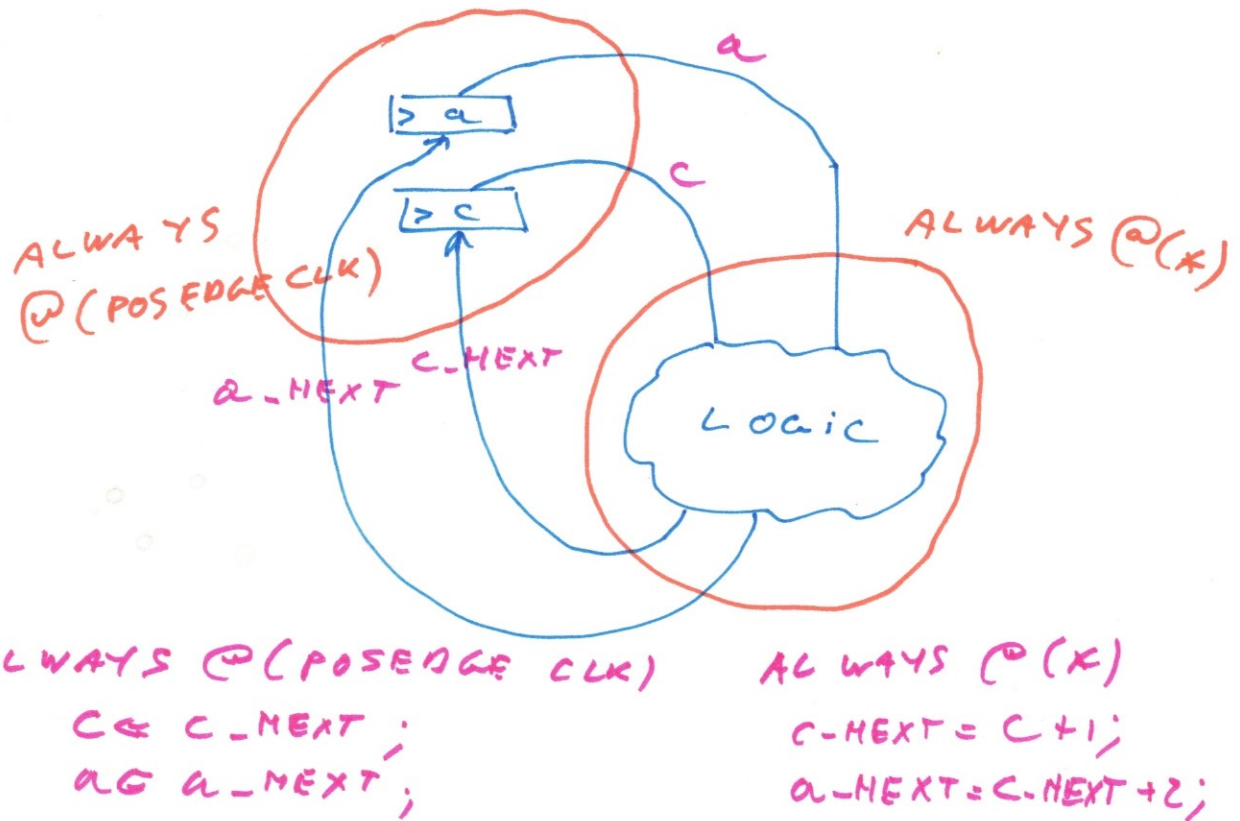
S0	x	x	x
S1	0	x	x
S2	0	0	x
S3	0	0	1



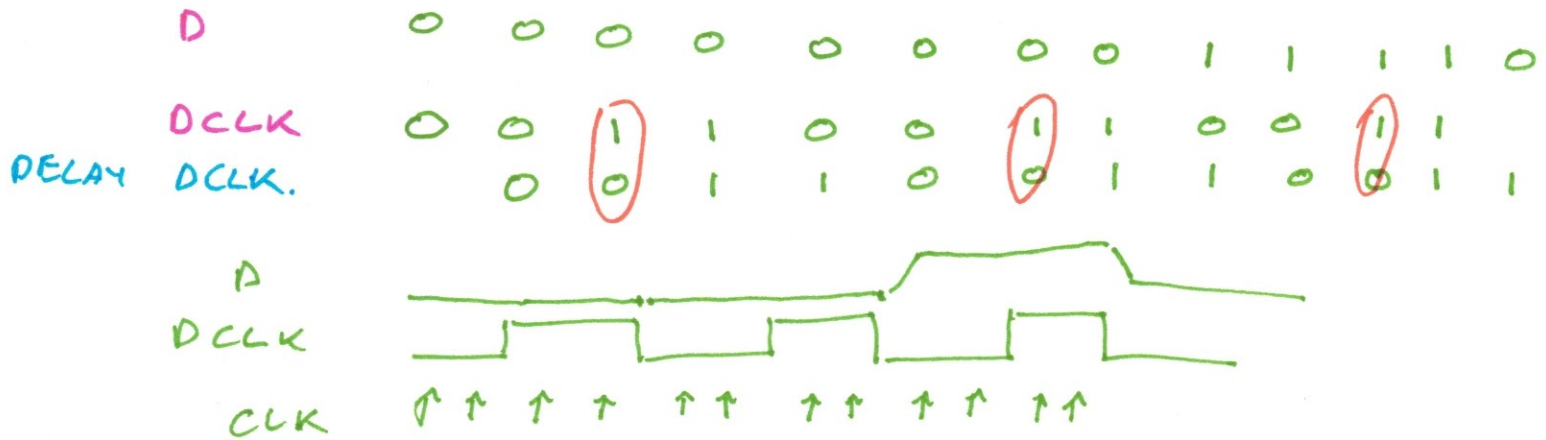
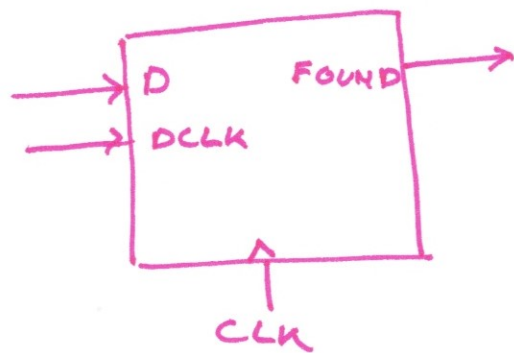
HARDWARE $\neq$ C

C:
 $c = c + 1;$
 $a = c + 2;$

HW:



PATTERN RECOGNIZER WITH DATA CLOCK.



$$DCLK \propto \sim \text{DELAY} DCLK$$

