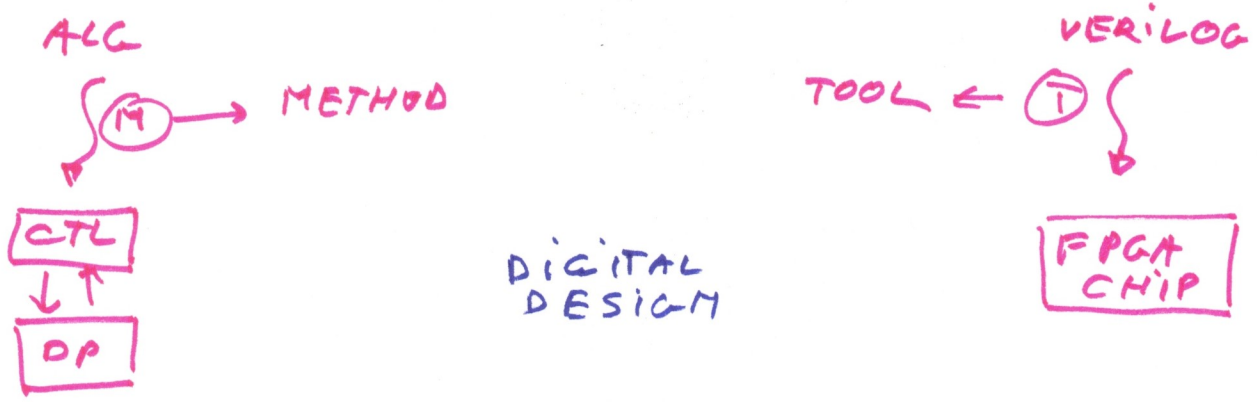




APPLICATION

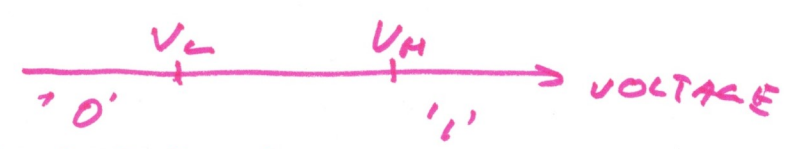
* FLOATING POINT ADDER

DIGITAL HARDWARE DESIGN

- SYSTEMATIC ENGINEERING ACTIVITY
- USES A DESIGN METHOD TO CONVERT
- HIGH-LEVEL BEHAVIORAL SPECIFICATION
- INTO A LOW-LEVEL TECHNOLOGY-SPECIFIC STRUCTURAL DESCRIPTION
- USING CONSTRAINTS SUCH PERFORMANCE, POWER, ...

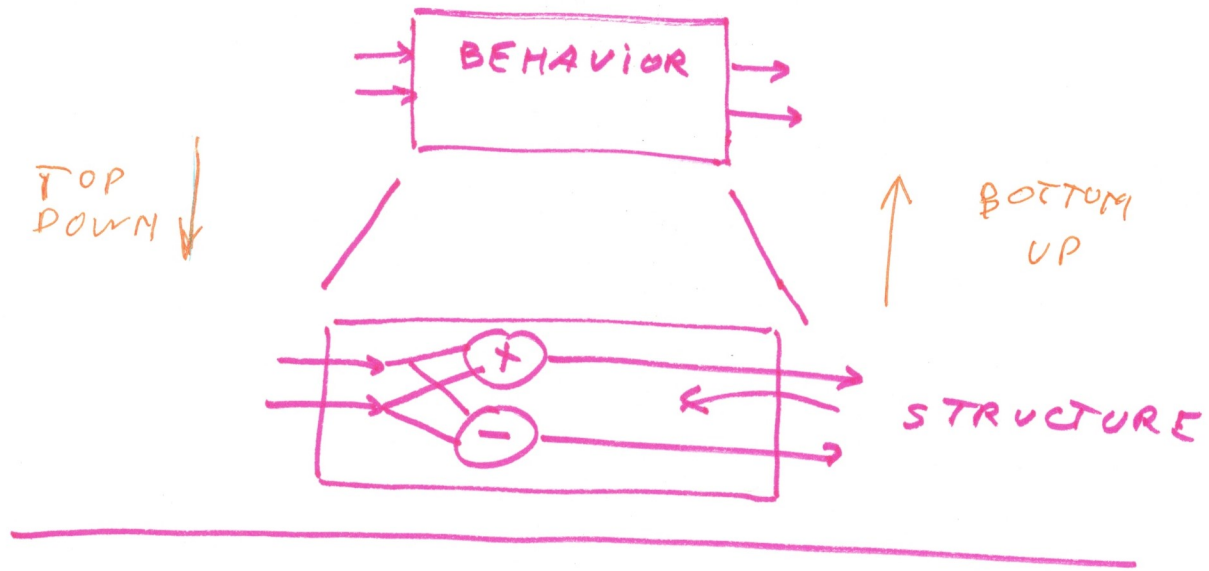
→ 'DIGITAL'?

① DISCRETE VOLTAGE LEVELS



② DISCRETE TIME STEPS





'HEART BEAT'

